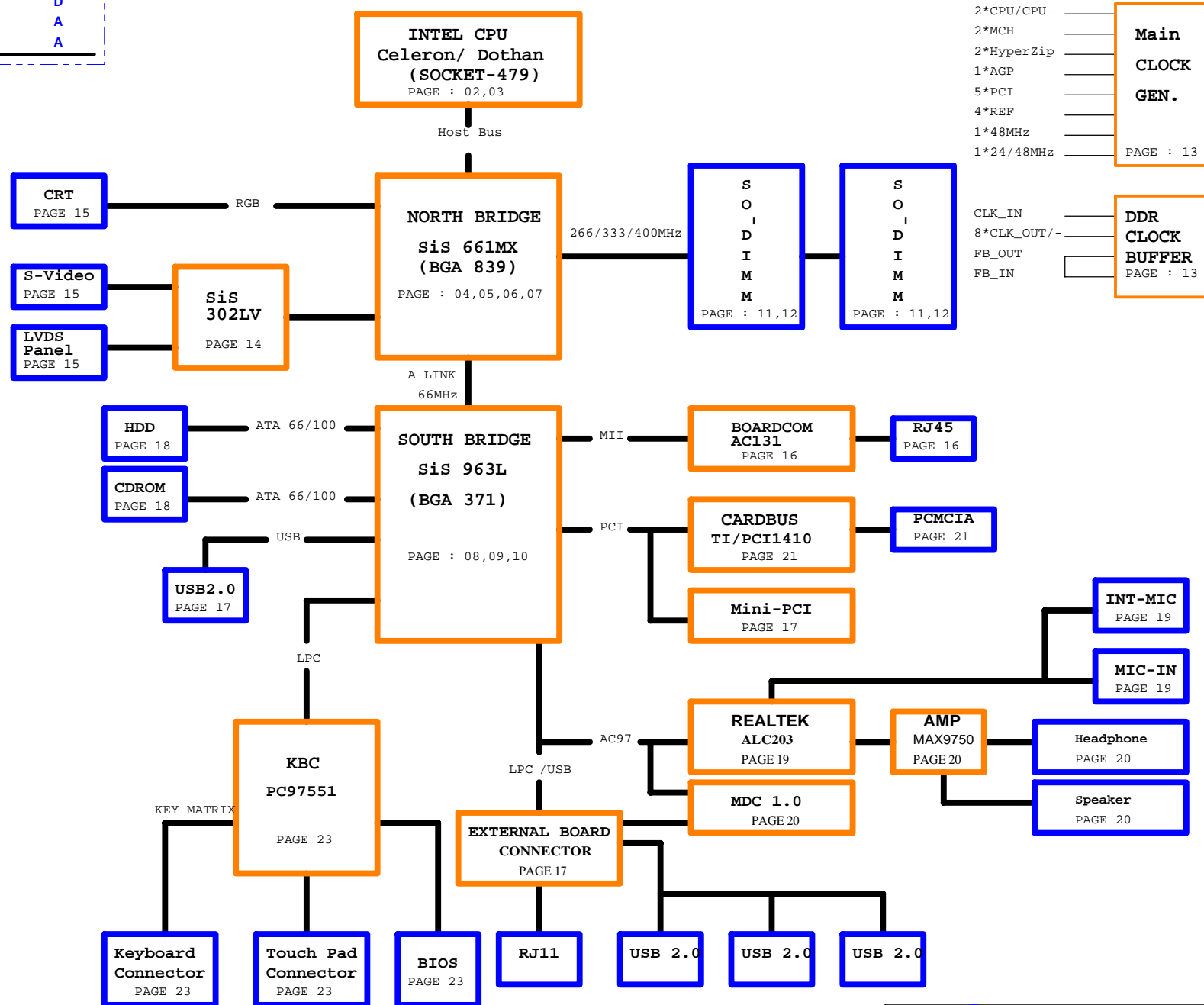


PCI DEVICES	IDSEL #	REQ/GNT #	INT
MINI-PCI	AD22	0	B,C
Card bus	AD17	1	D
Video bridge	N/A	N/A	A
VGA	N/A	N/A	A



0502 U41 CHANGE COMPONENT

U41A

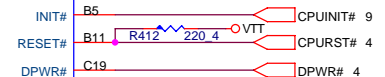
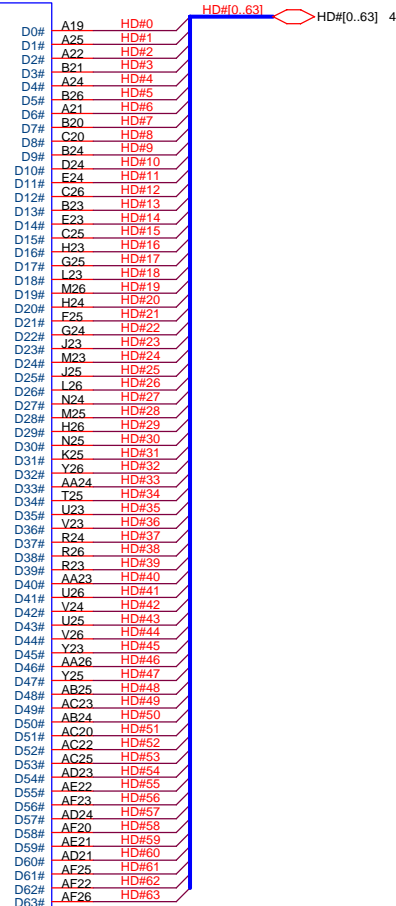
Banias

1 OF 3

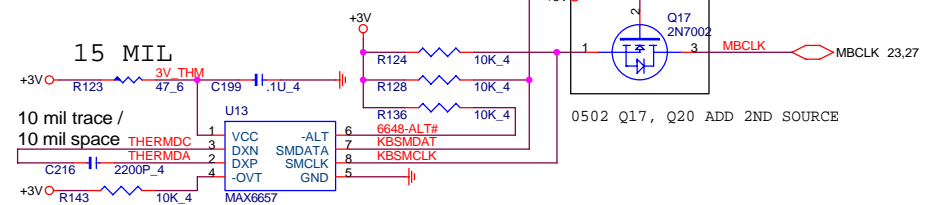
REQUEST  
PHASE  
SIGNALSDATA  
PHASE  
SIGNALSERROR  
SIGNALSARBITRATION  
PHASE  
SIGNALSSNOOP PHASE  
SIGNALSRESPONSE  
PHASE  
SIGNALSPC  
COMPATIBILITY  
SIGNALSDIAGNOSTIC  
& TEST  
SIGNALSEXECUTION  
CONTROL  
SIGNALS

THERMAL DIODE

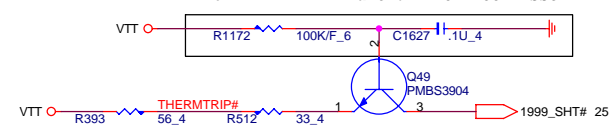
Banias\_Processor



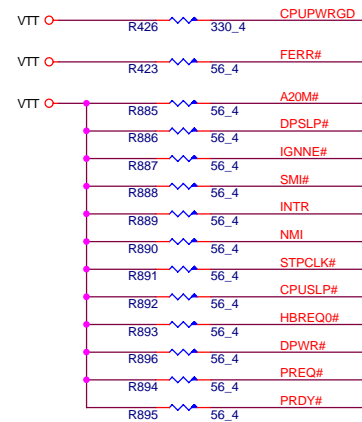
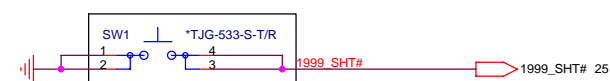
## H/W MONITOR



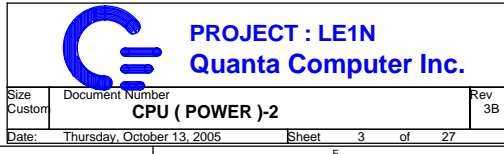
0427 ADD R1172 &amp; C1627 FOR BOOT ISSUE



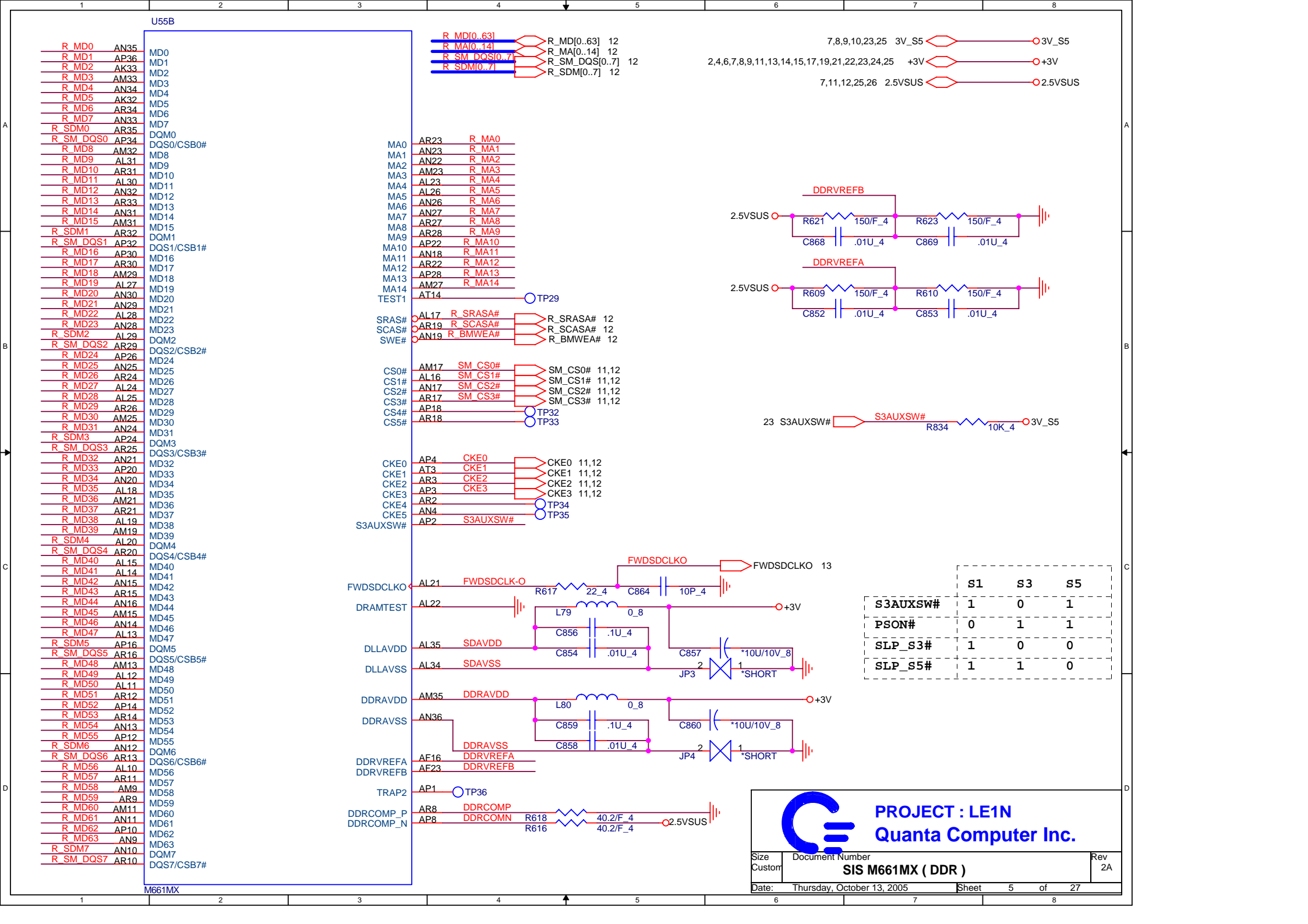
0426 SW1 UNSTAFF

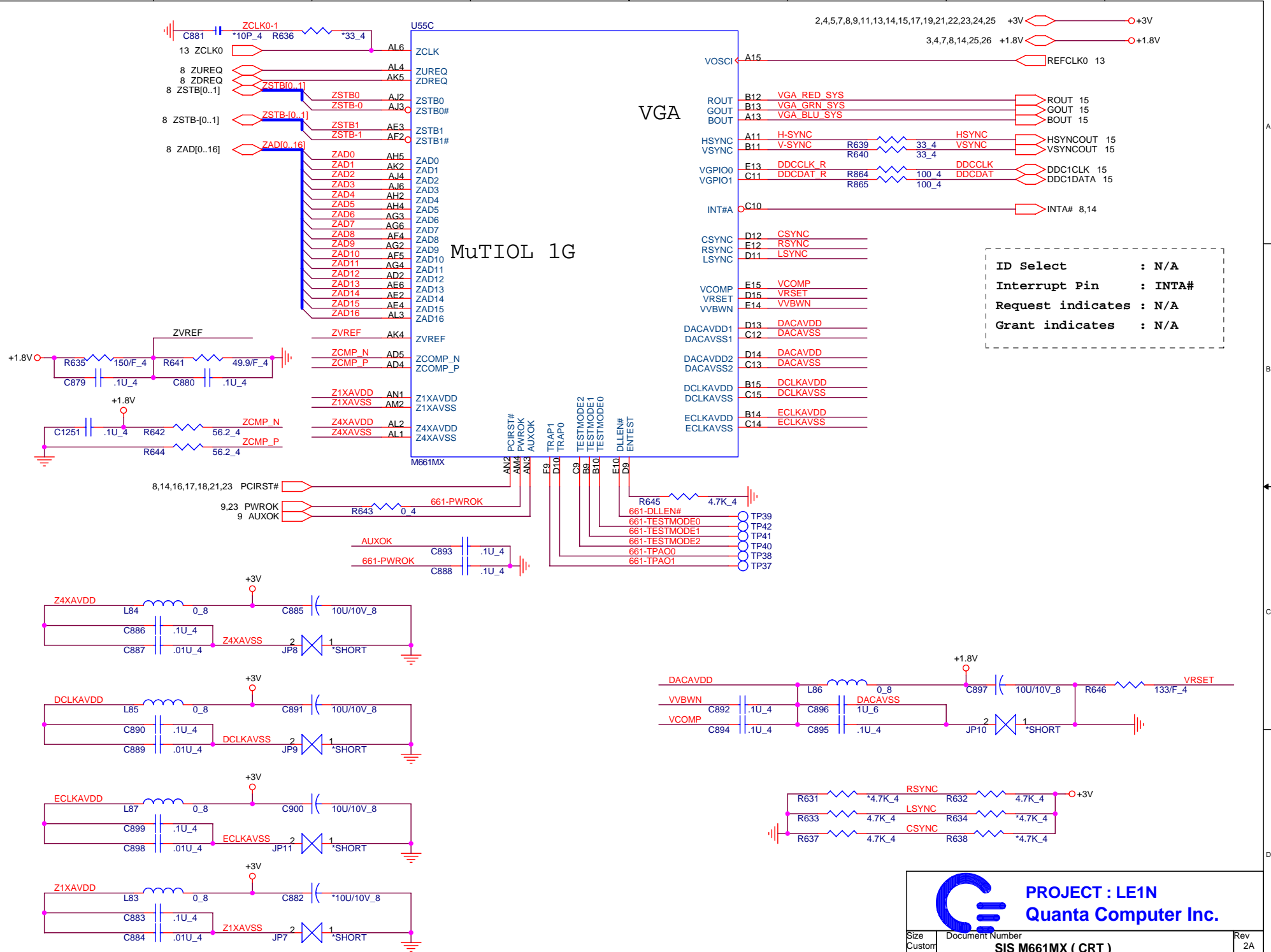
PROJECT : LE1N  
Quanta Computer Inc.

Size	Document Number	Rev
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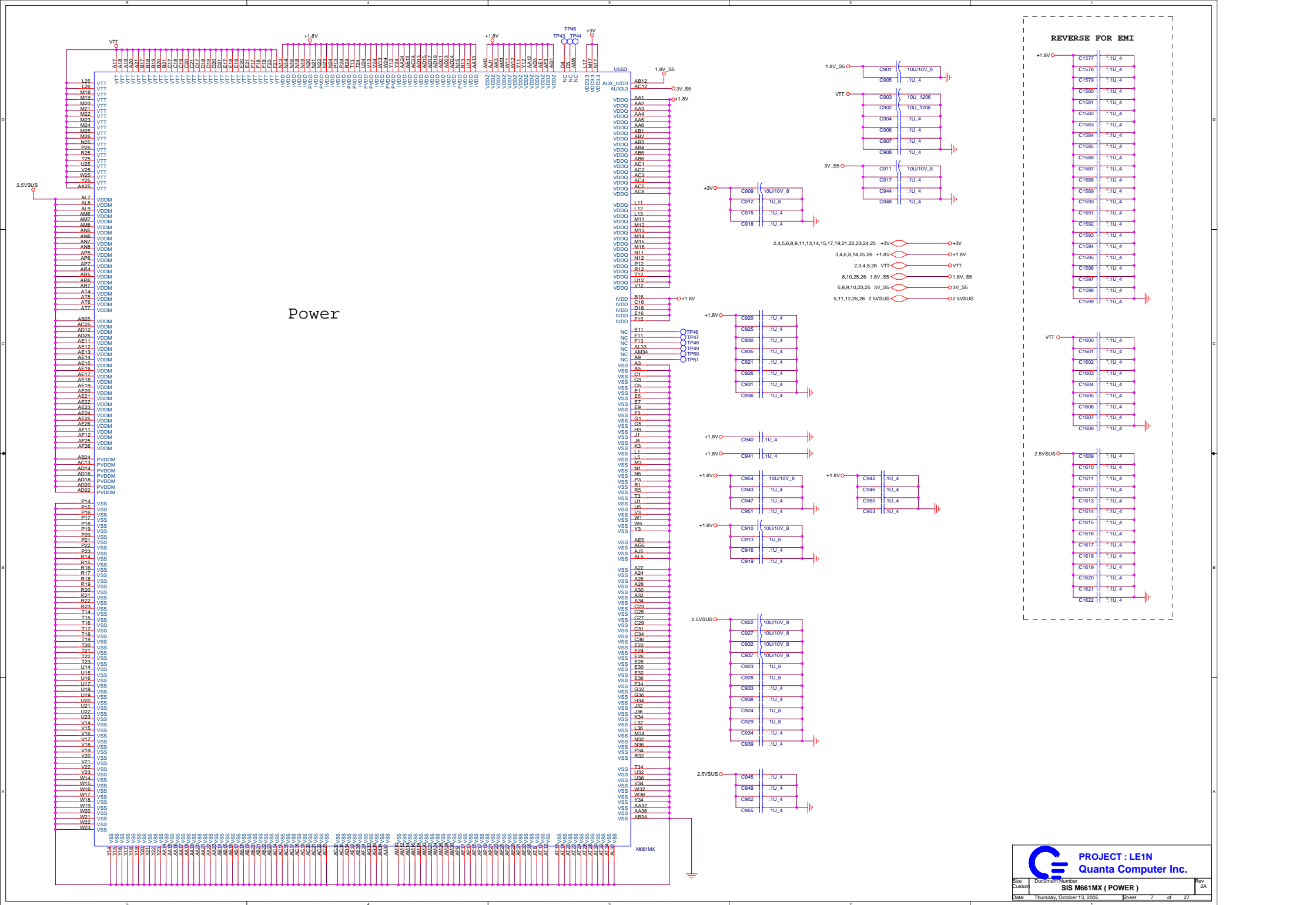




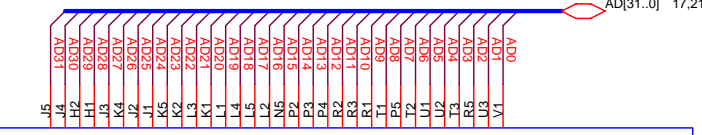
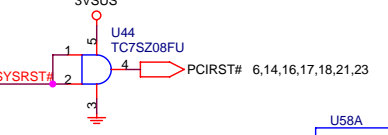
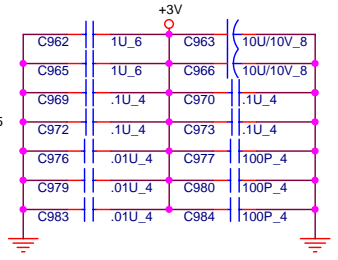
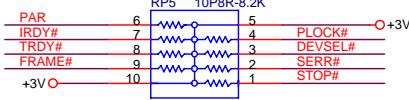
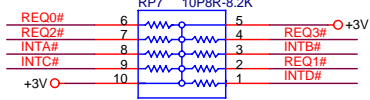
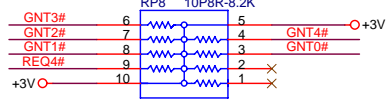




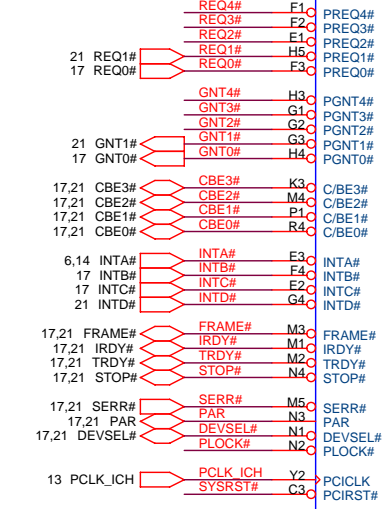




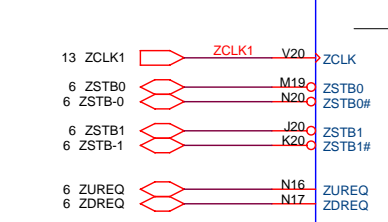
# PCI PULL UP



## PCI

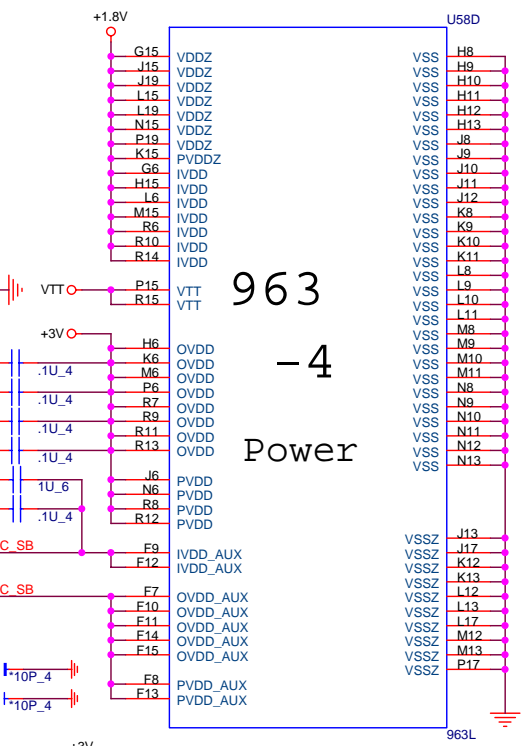
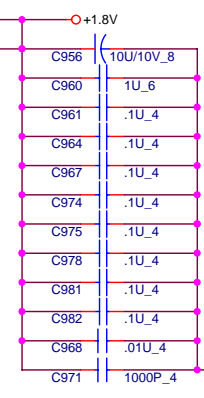
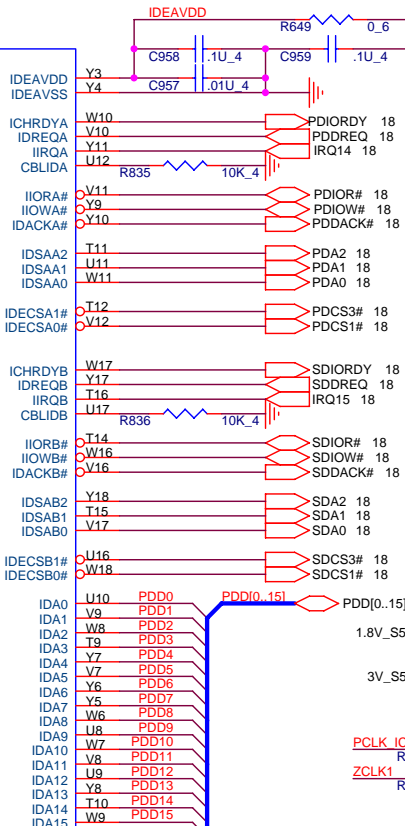
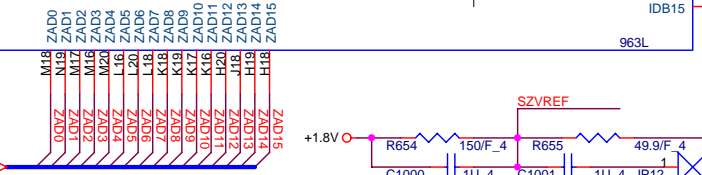
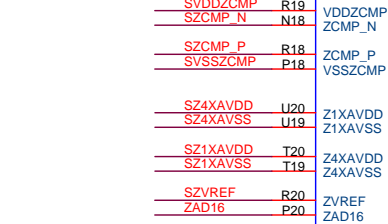


## IDE

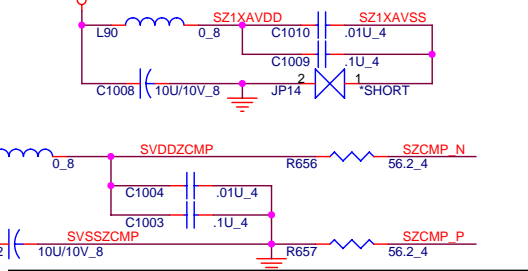


## 963-1

## HyperZip

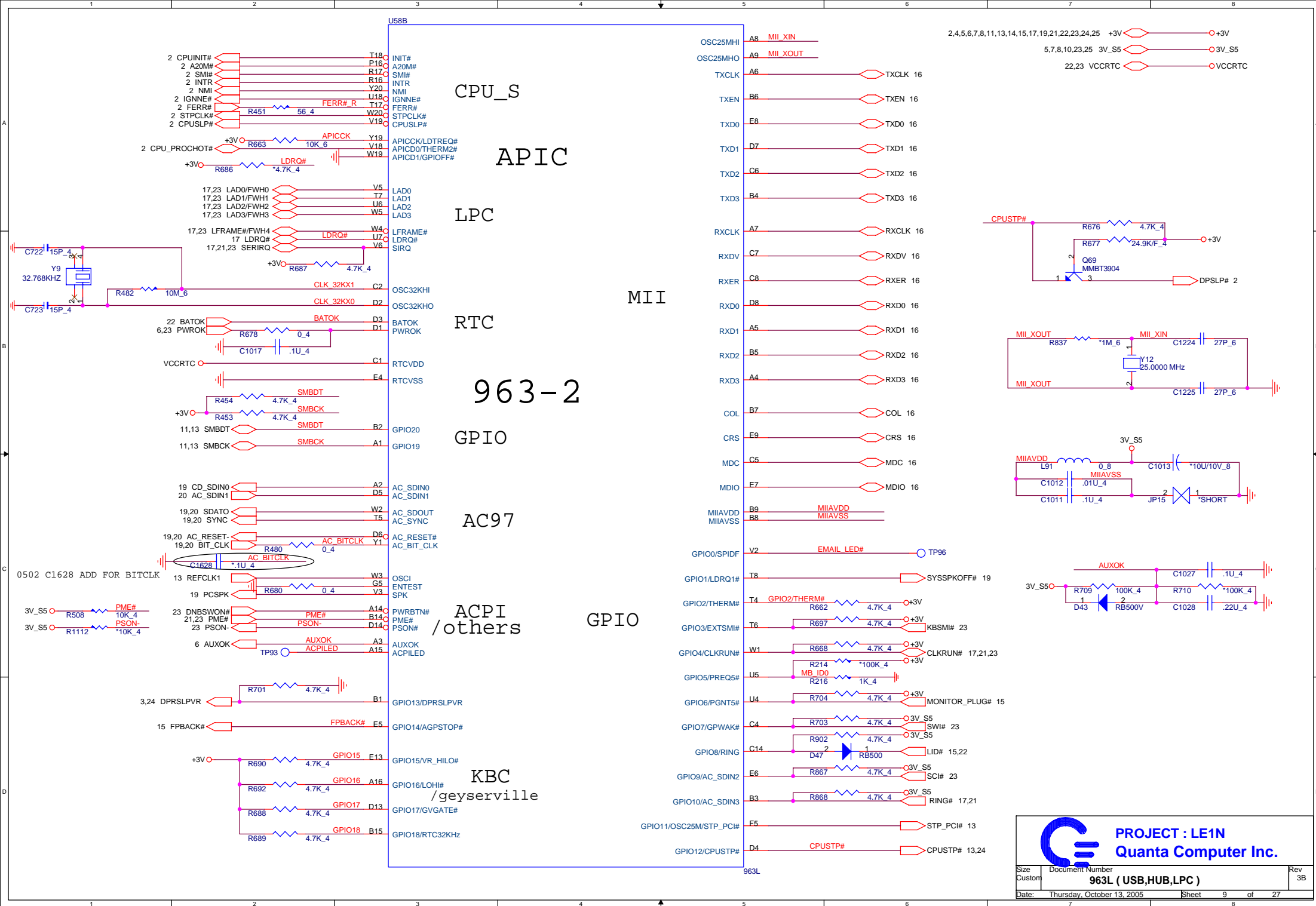


## 963-4 Power

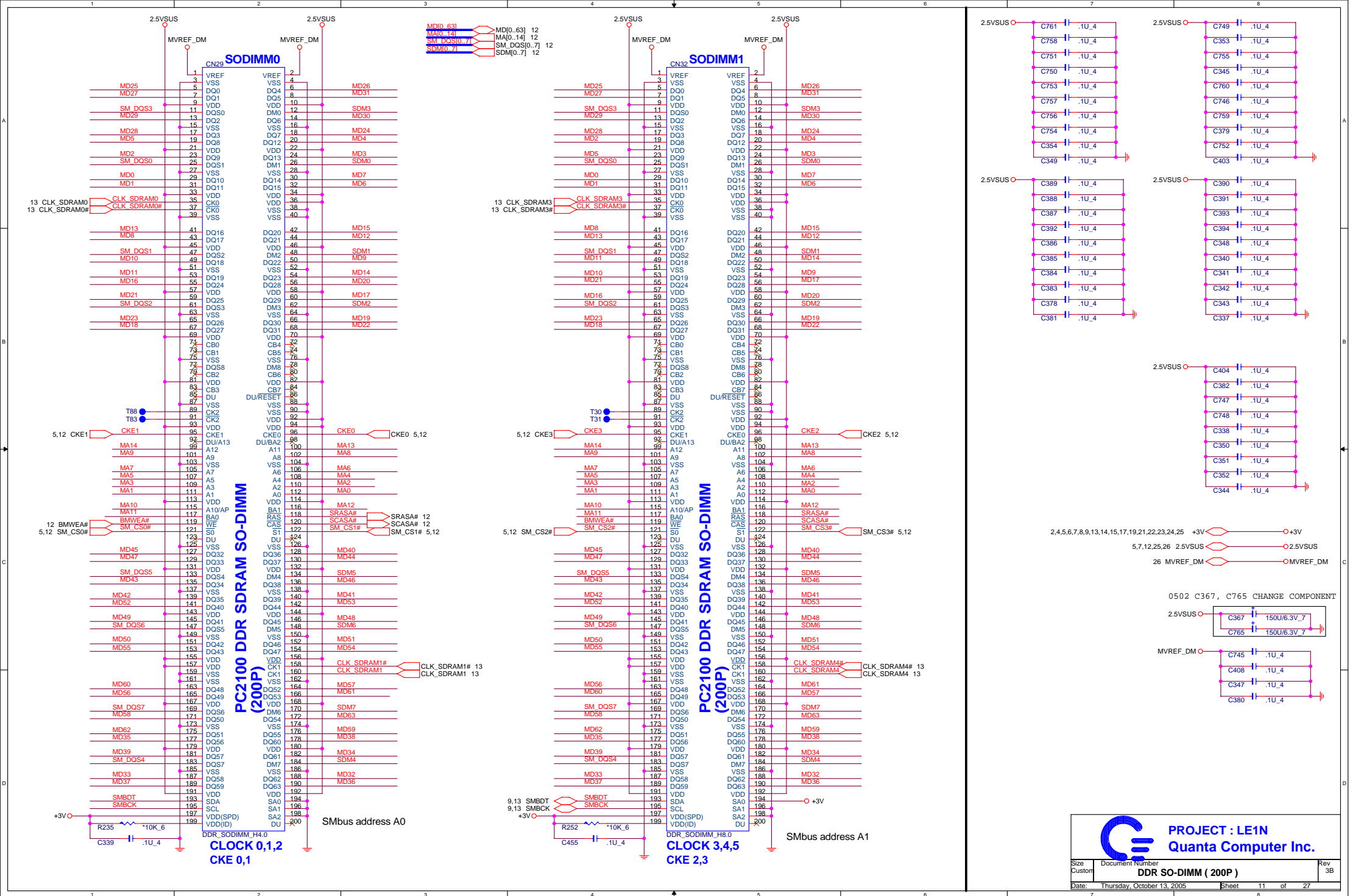


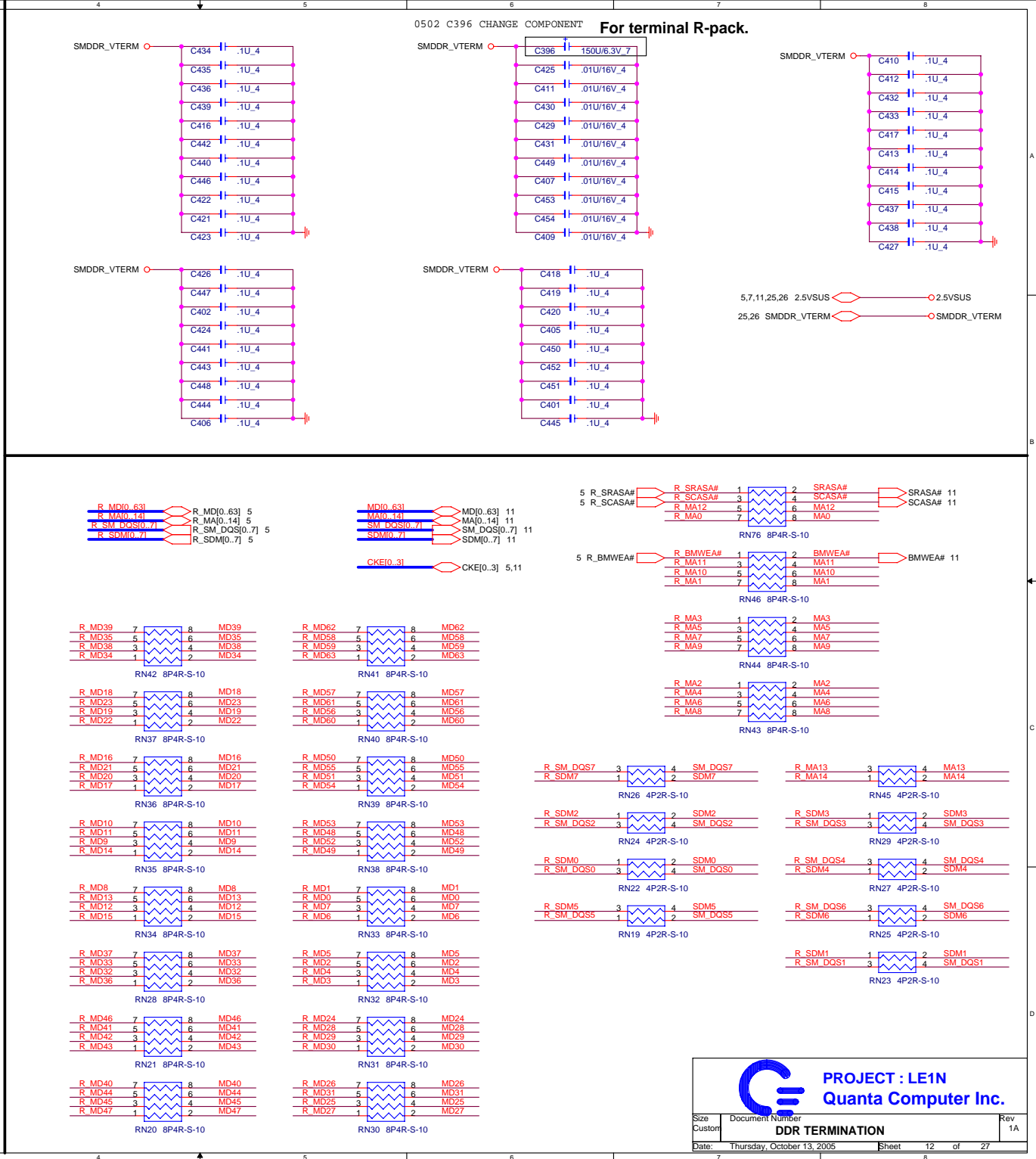
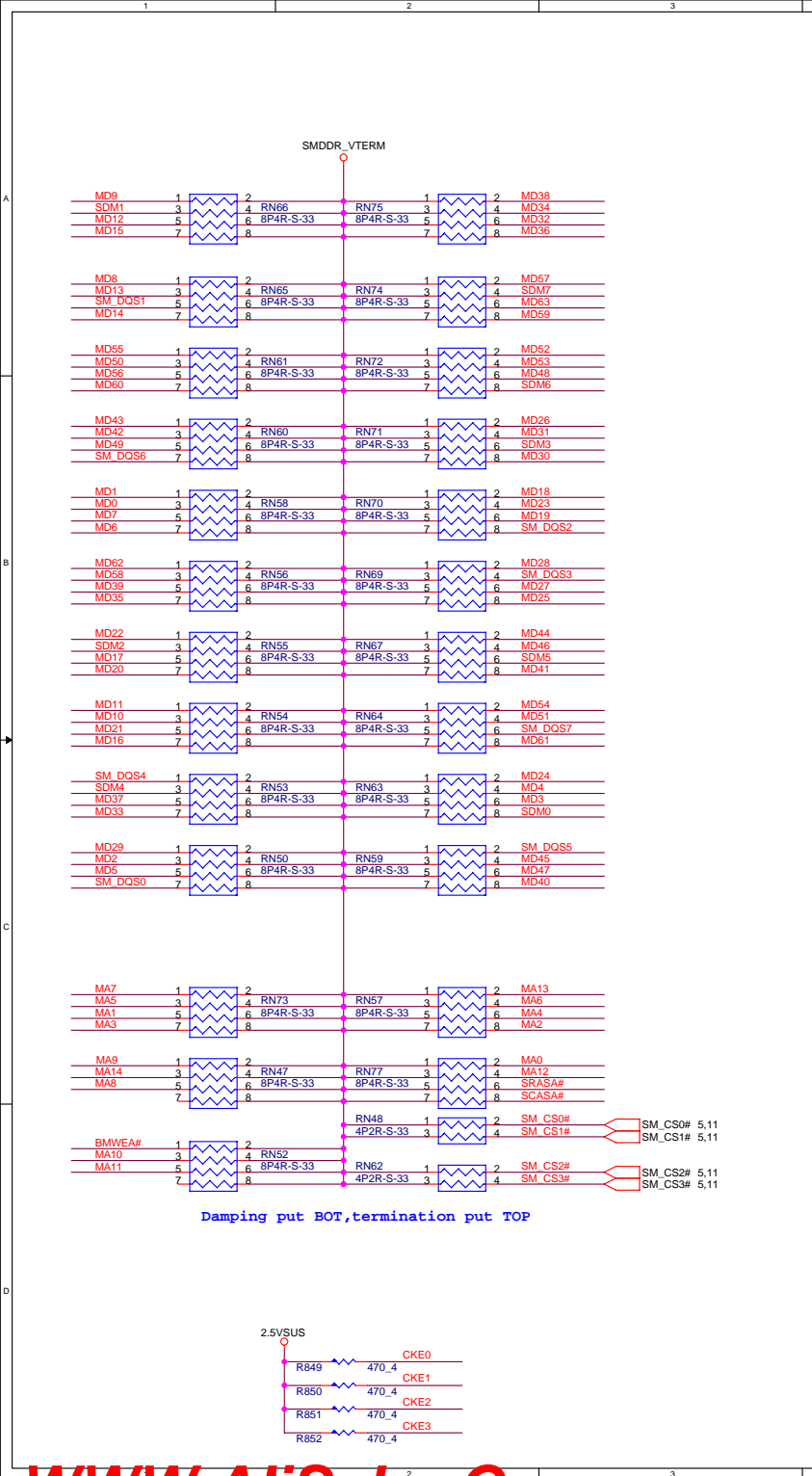
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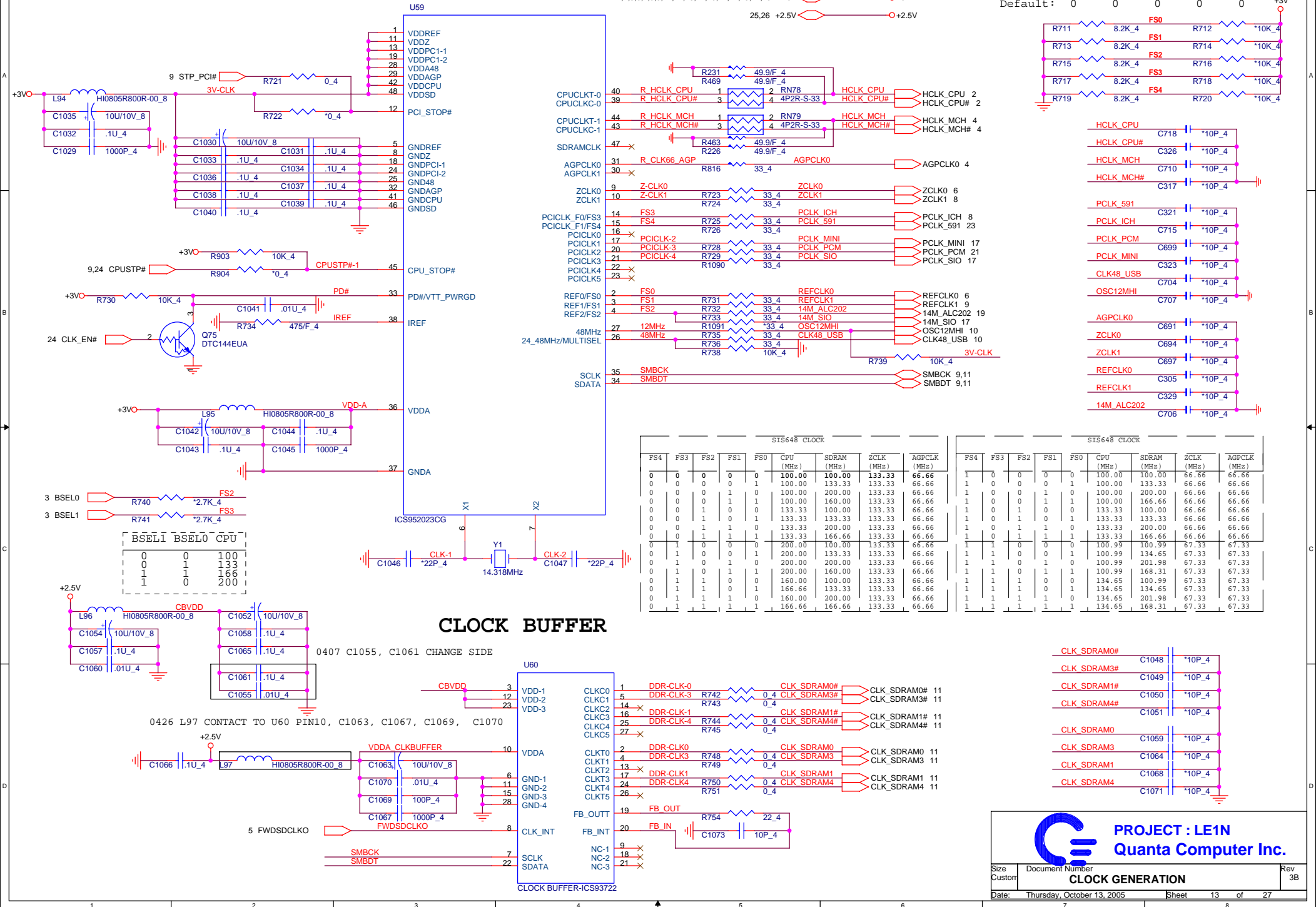


# MAIN CLOCK GENERATOR

3,4,6,7,8,14,25,26 +1.8V  
2,4,5,6,7,8,9,11,14,15,17,19,21,22,23,24,25 +3V  
25,26 +2.5V

## Frequency Selection

FS4 FS3 FS2 FS1 FS0  
Default: 0 0 0 0 0

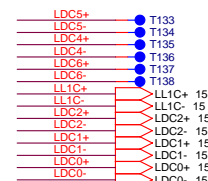
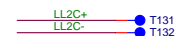
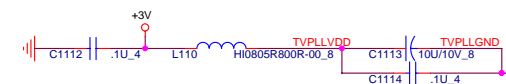
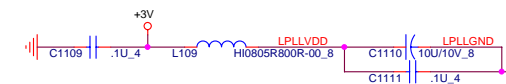
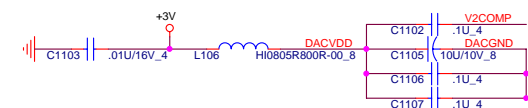
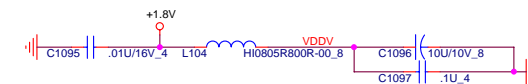
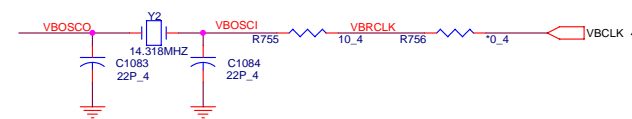


**PROJECT : LE1N**  
**Quanta Computer Inc.**

3,4,6,7,8,25,26 +1.8V

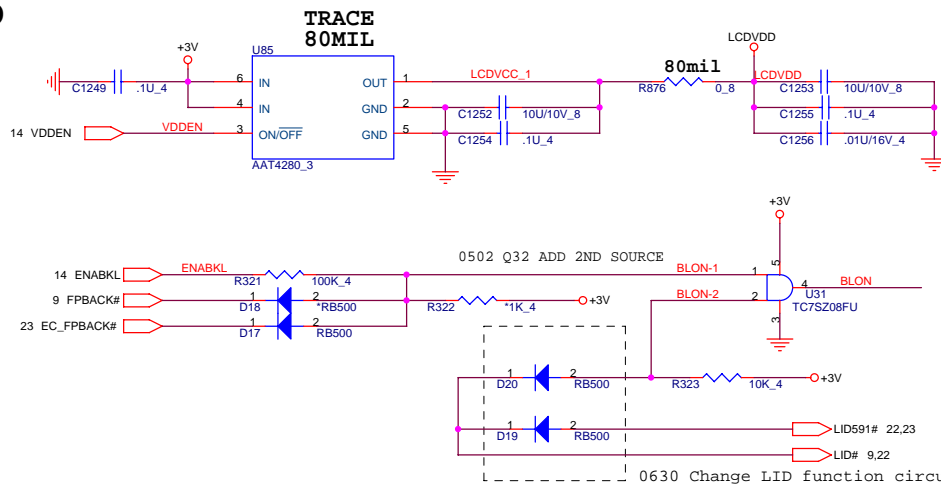
2,4,5,6,7,8,9,11,13,15,17,19,21,22,23,24,25 +3V

15,17,18,20,21,22,23,24,25 +5V

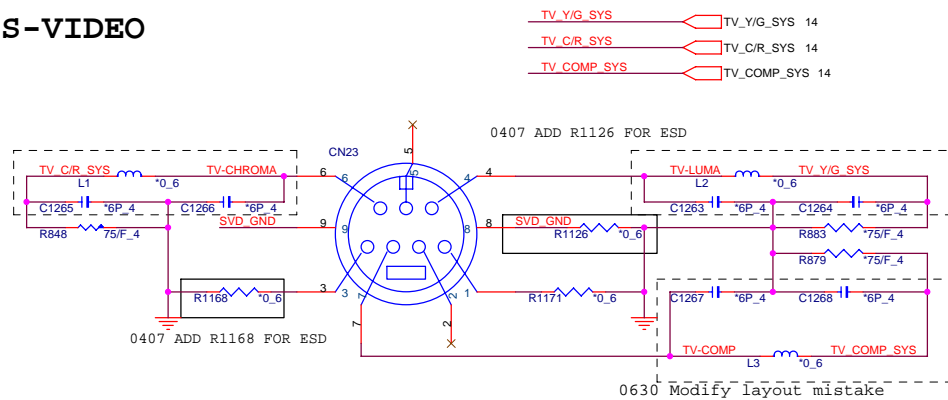




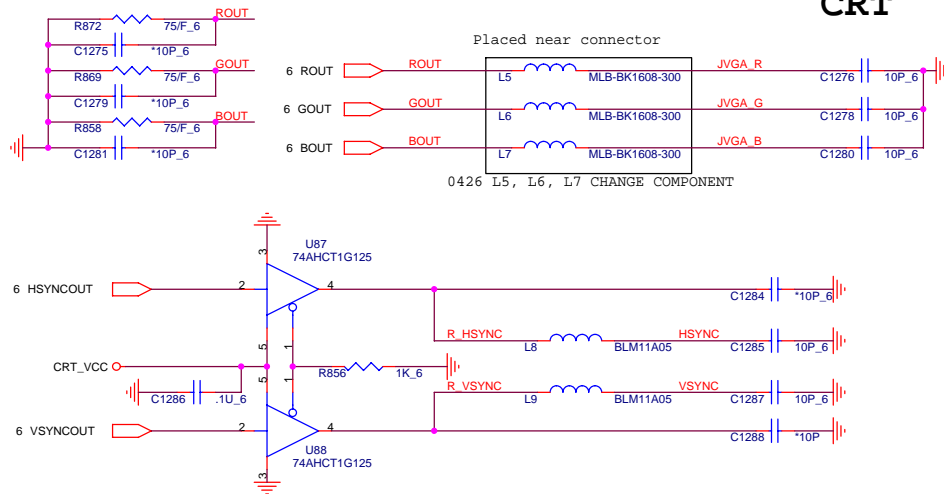
# LCD



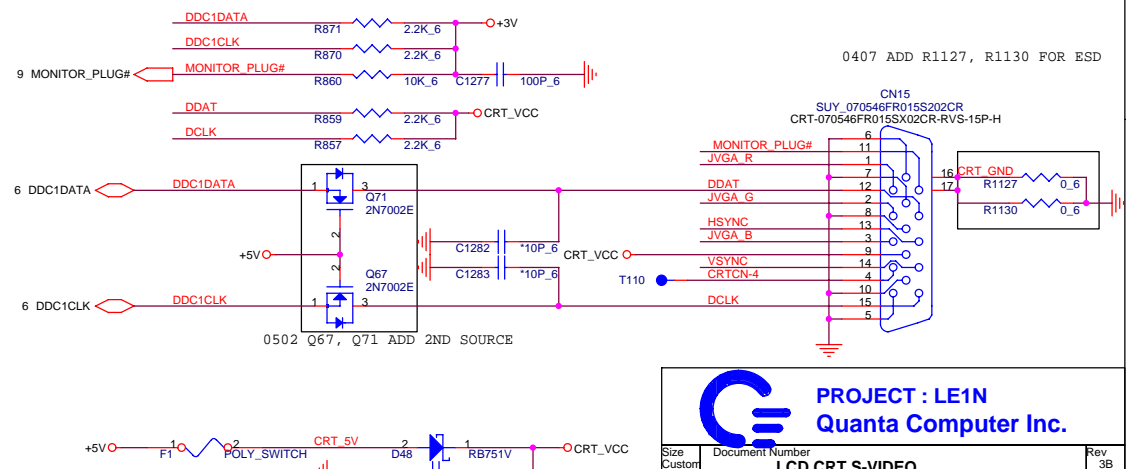
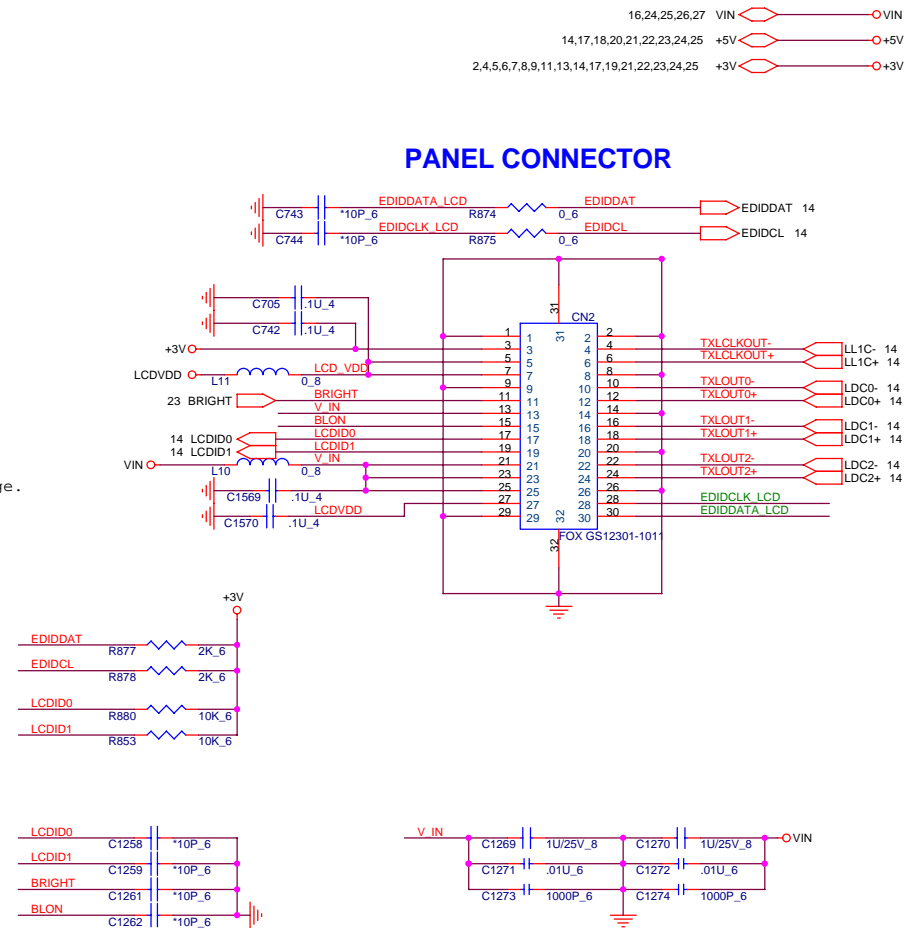
## S-VIDEO

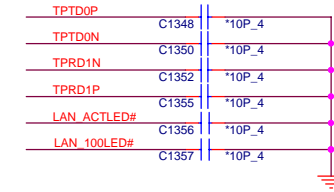


## CRT

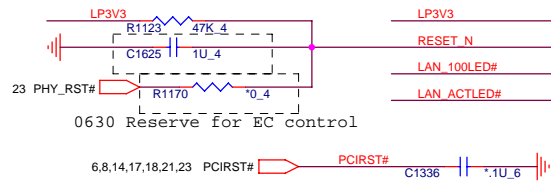


## PANEL CONNECTOR

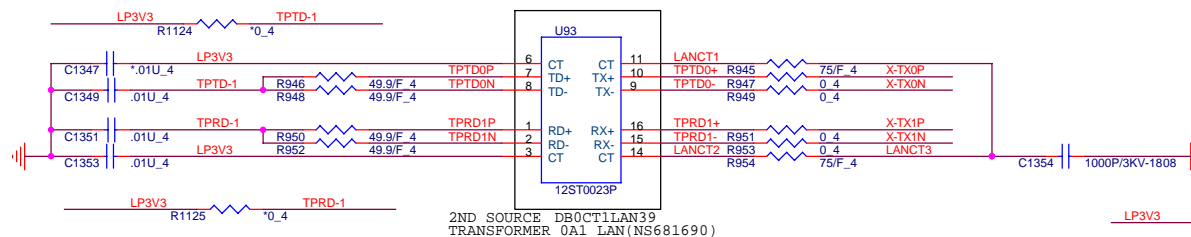




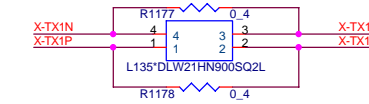
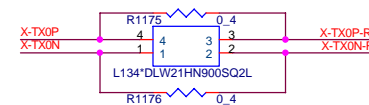
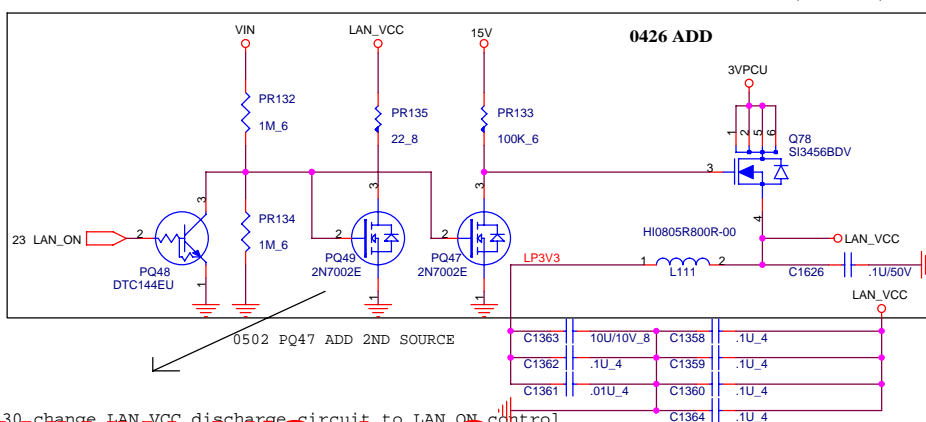
0630 Delay RC timing



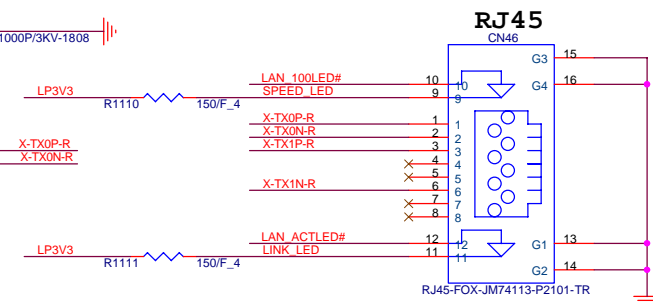
0426 U93 CHANGE COMPONENT



2ND SOURCE DB0CT1LAN39  
TRANSFORMER 0A1 LAN(NS681690)



1005 reserve 2 common  
choke,must near LAN connector



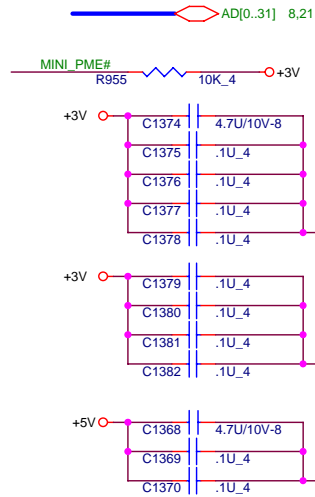
0630 change footprint for PE.



**PROJECT : LE1N**  
**Quanta Computer Inc.**

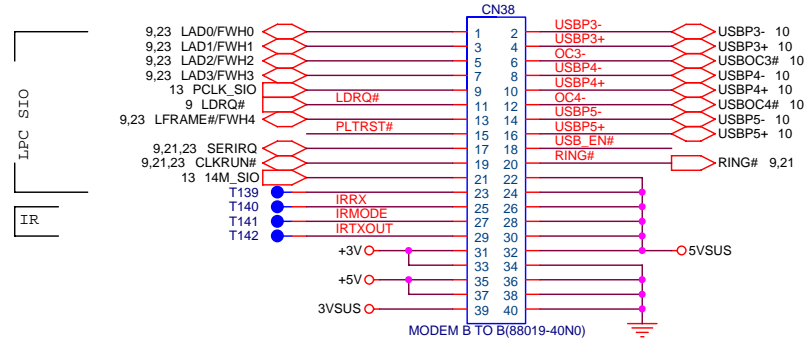
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2,4,5,6,7,8,9,11,13,14,15,19,21,22,23,24,25 +3V  
14,15,18,20,21,22,23,24,25 +5V  
25,26 5VSUS  
8,10,20,23,25,26 3VSUS



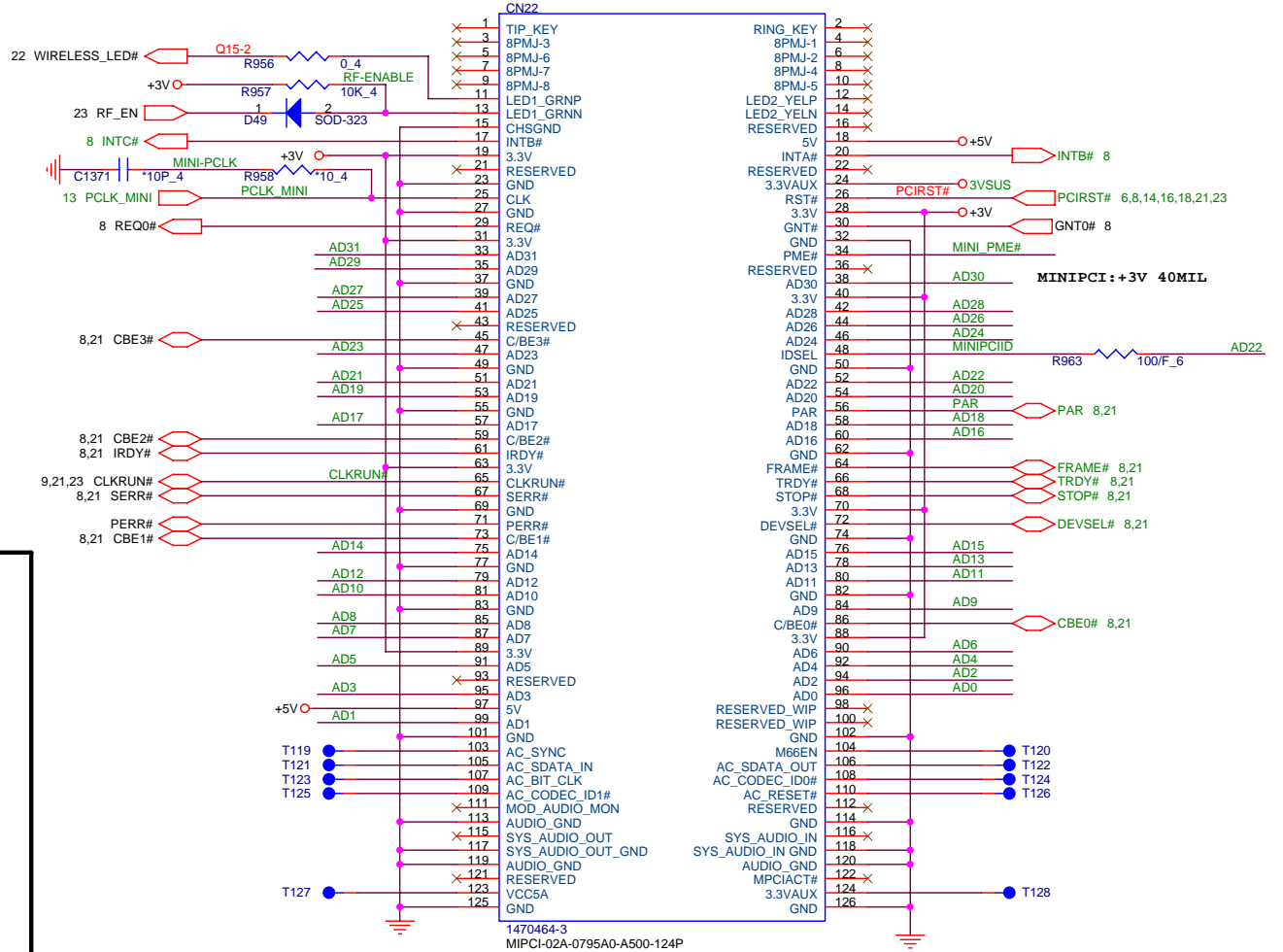
PCIRST# PLTRST#  
R224 0.4

### M/B TO MODEM/B

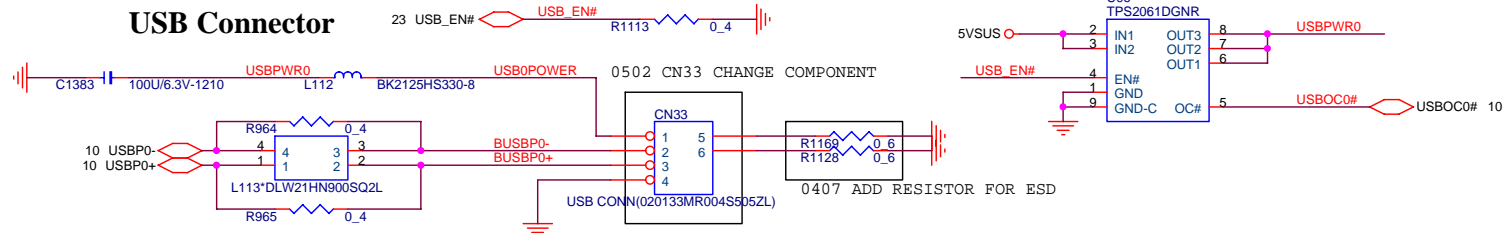


## INTB# INTC# , REQ0# TYPE III MINI PCI SOCKET

0502 CN22 CHANGE COMPONENT

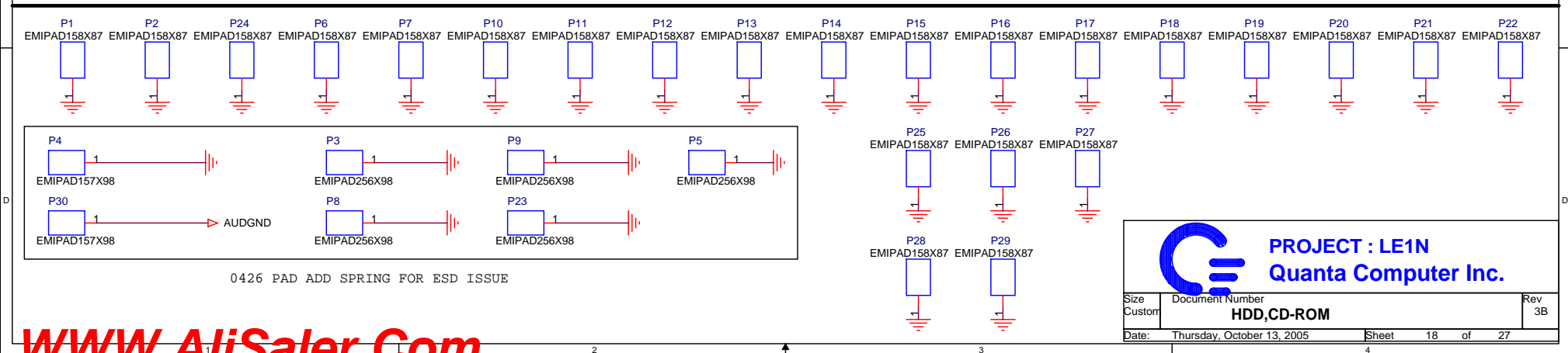
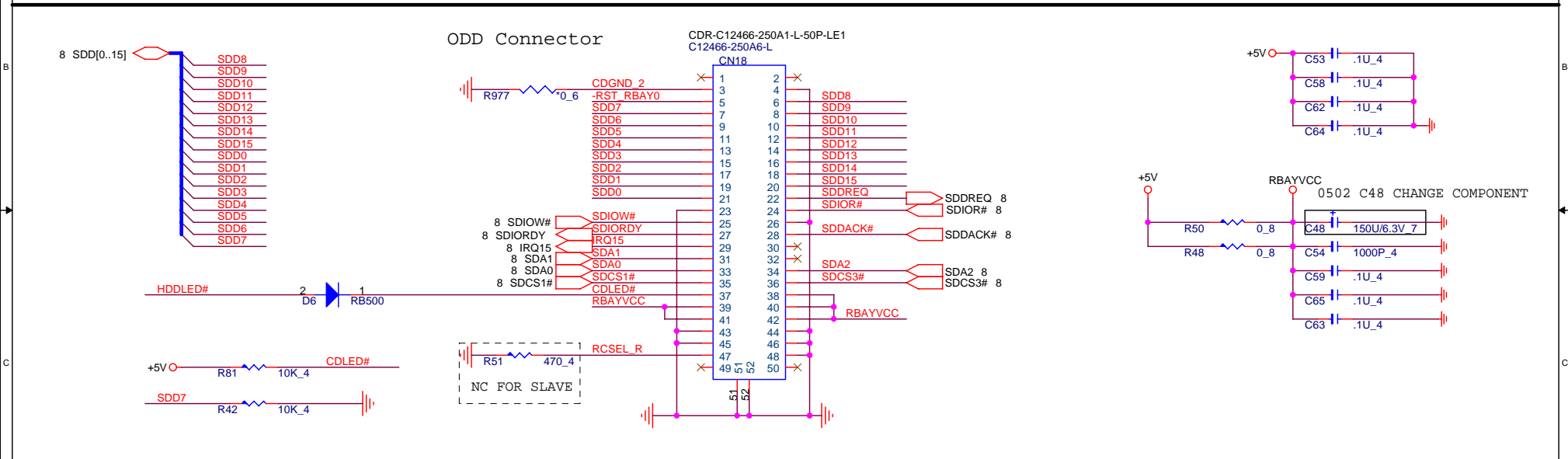
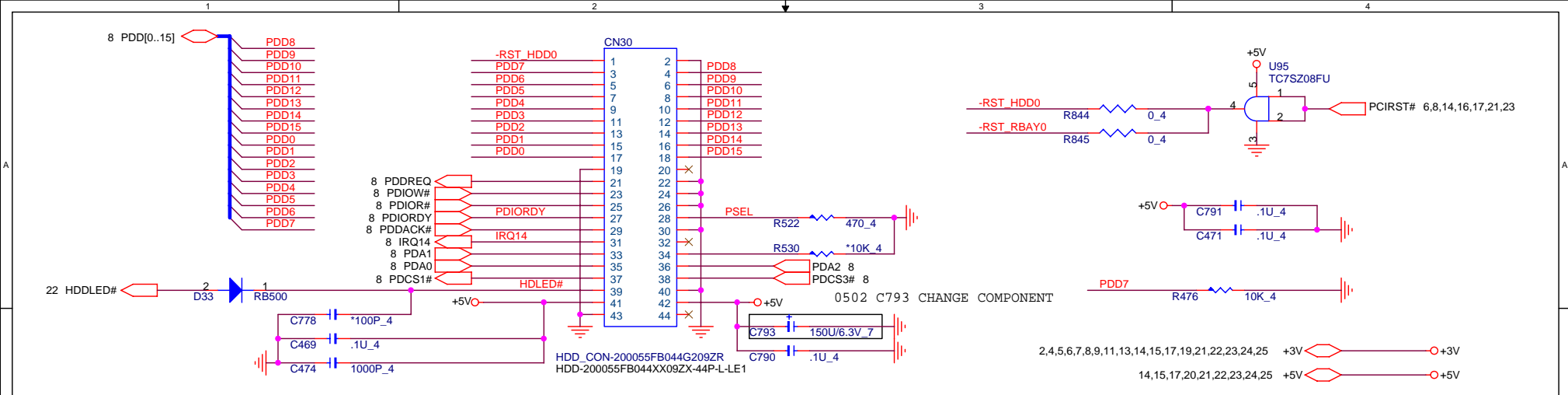


### USB Connector

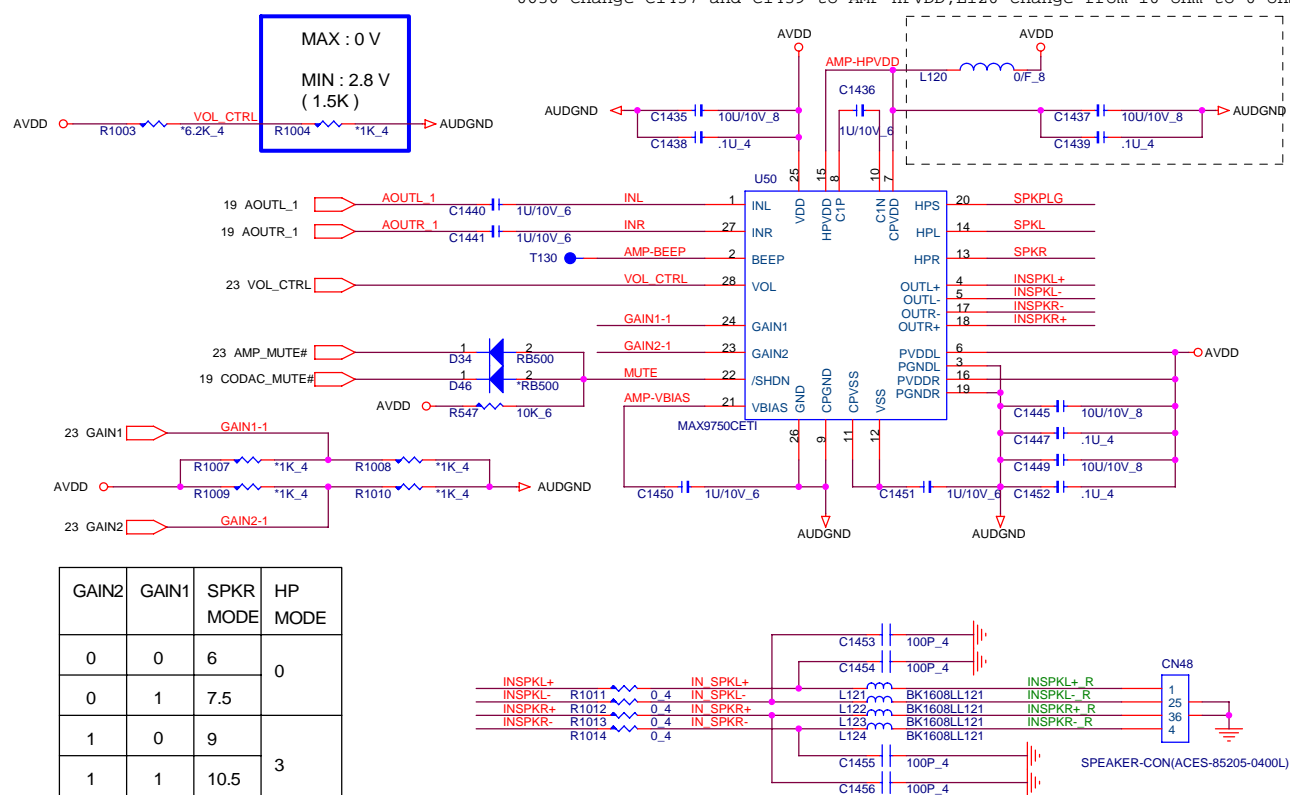


PROJECT : LE1N  
Quanta Computer Inc.

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Custom	MINI-PCI & USB	3B
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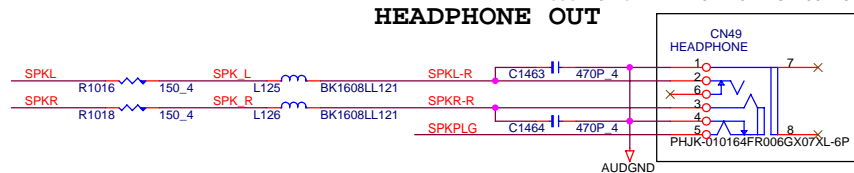




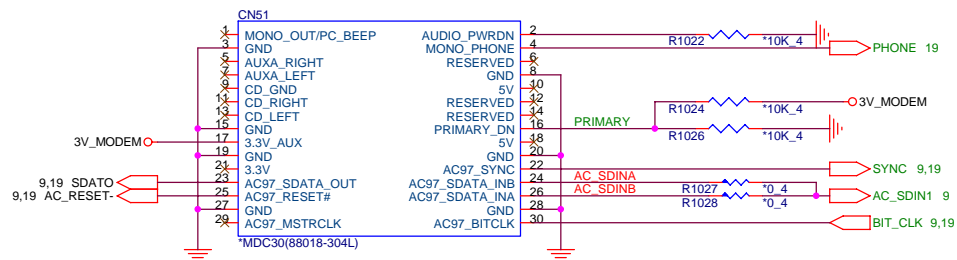


## HEADPHONE OUT

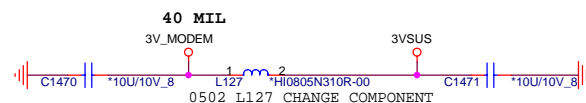
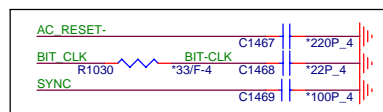
0502 CN49 HEADPHONE CHANGE COMPONENT



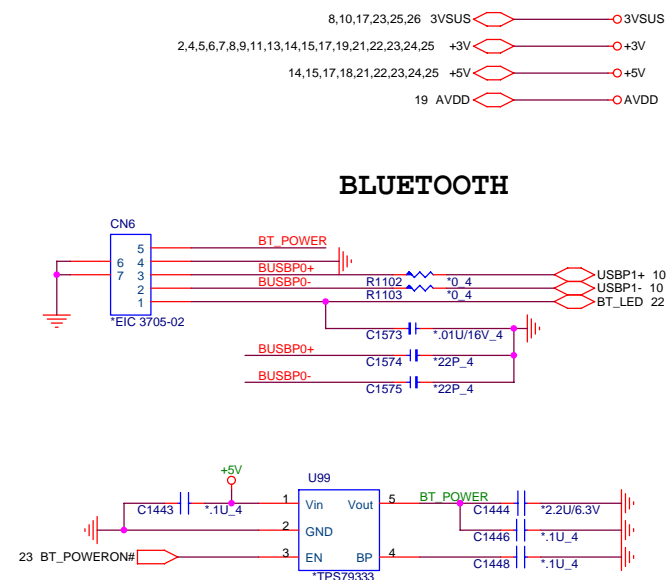
## MDC



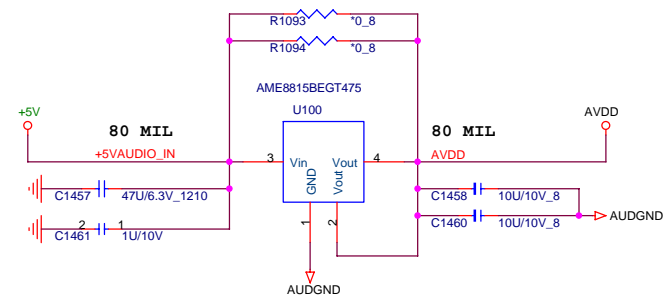
0413 R1030, C1467, C1468, C1469 UNSTAFF



## BLUETOOTH



## AUDIO POWER



PROJECT : LE1N  
Quanta Computer Inc.

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AUDIO AMP, MDC, PWR & BEEP		
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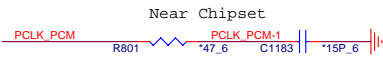


Grant Indicate : GNT1#

2ND SOURCE AJ014100F31  
IC(144P) PCI1410ASPGE(LQFP)

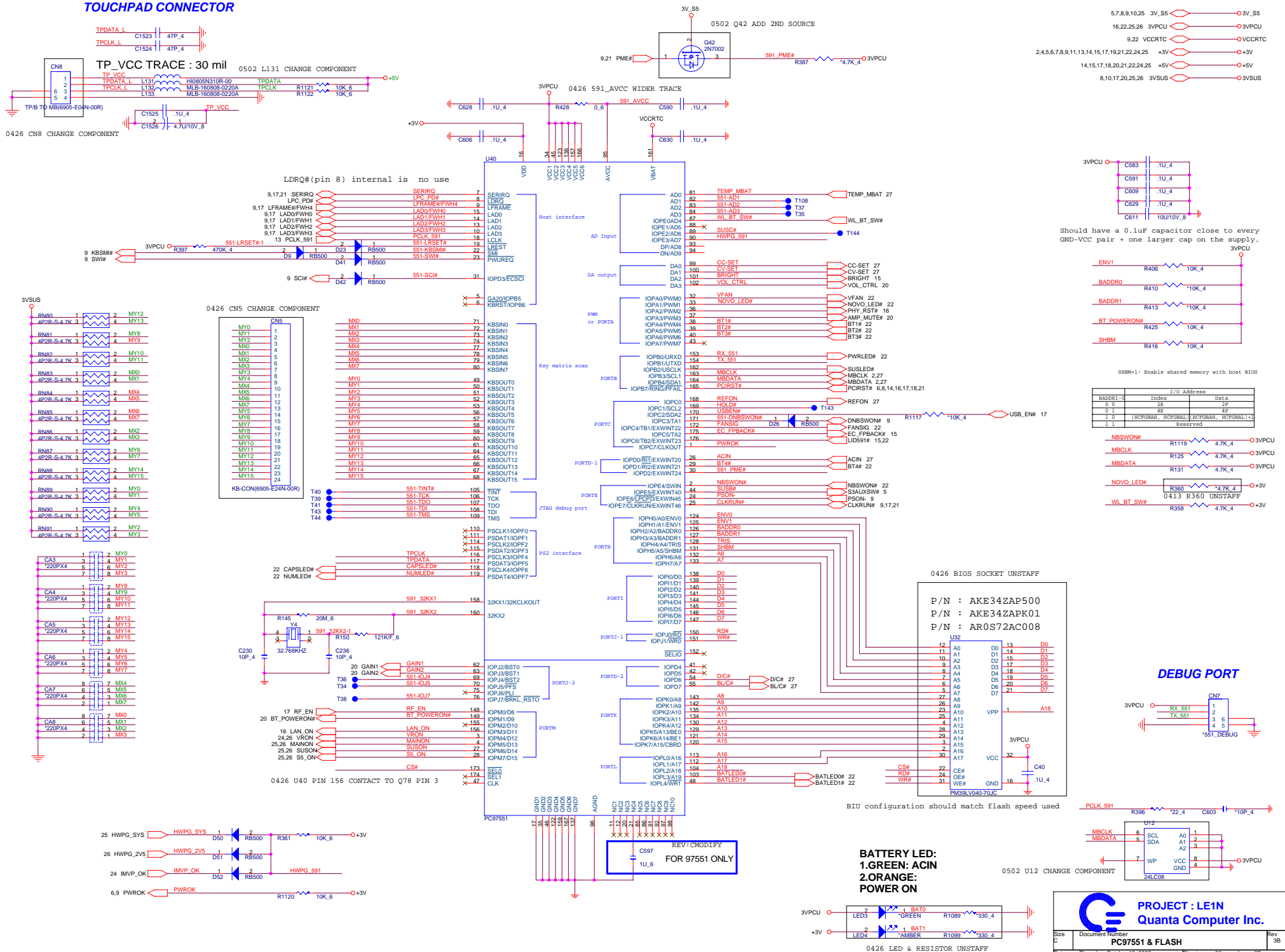
U64                      Near TPS2211A

14,15,17,18,20,22,23,24,25 +5V  +5V

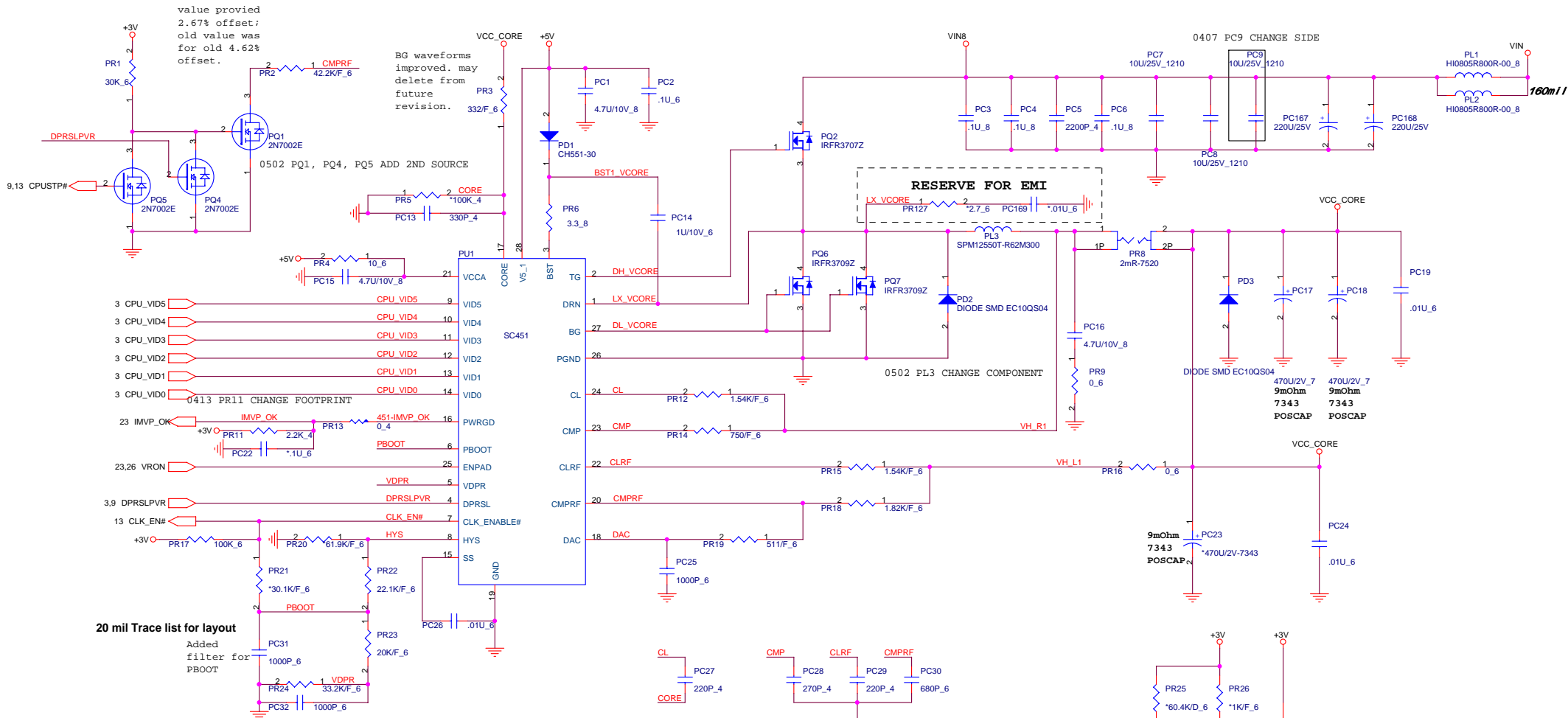




## TOUCHPAD CONNECTOR



value provided  
2.67% offset;  
old value was  
for old 4.62%  
offset.



V I D						Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0	V
0	1	0	1	1	1	1.340
0	1	1	0	0	0	1.324
0	1	1	0	1	0	1.292
0	1	1	1	0	0	1.260
0	1	1	1	0	1	1.244
0	1	1	1	1	1	1.212
1	0	0	0	0	1	1.180
1	0	0	0	1	1	1.148
1	0	0	1	1	0	1.100
1	0	1	0	0	1	1.052
1	0	1	0	1	1	1.020
1	0	1	1	1	0	0.972
1	1	0	0	0	0	0.940

**PROJECT : LE1N**  
**Quanta Computer Inc.**

Size Custom Document Number **CPU CORE (SC451)** Rev 3B

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